

Fig. 1. (Prior Art)

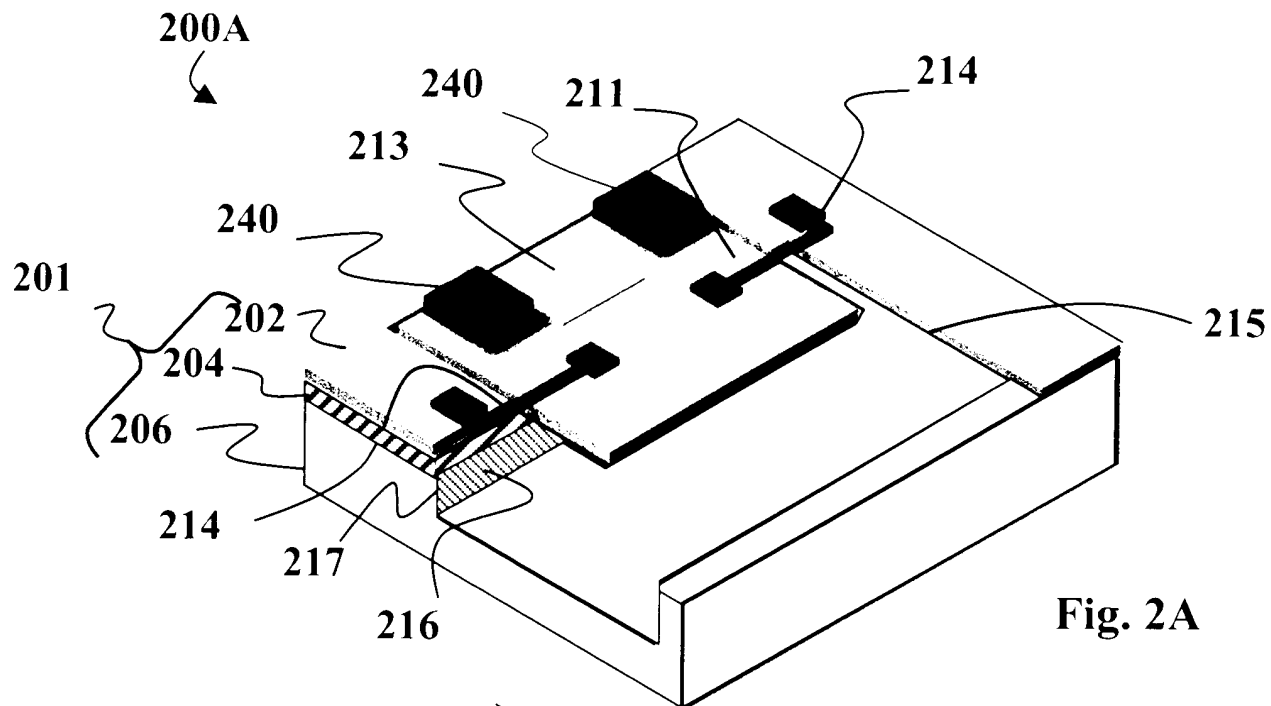


Fig. 2A

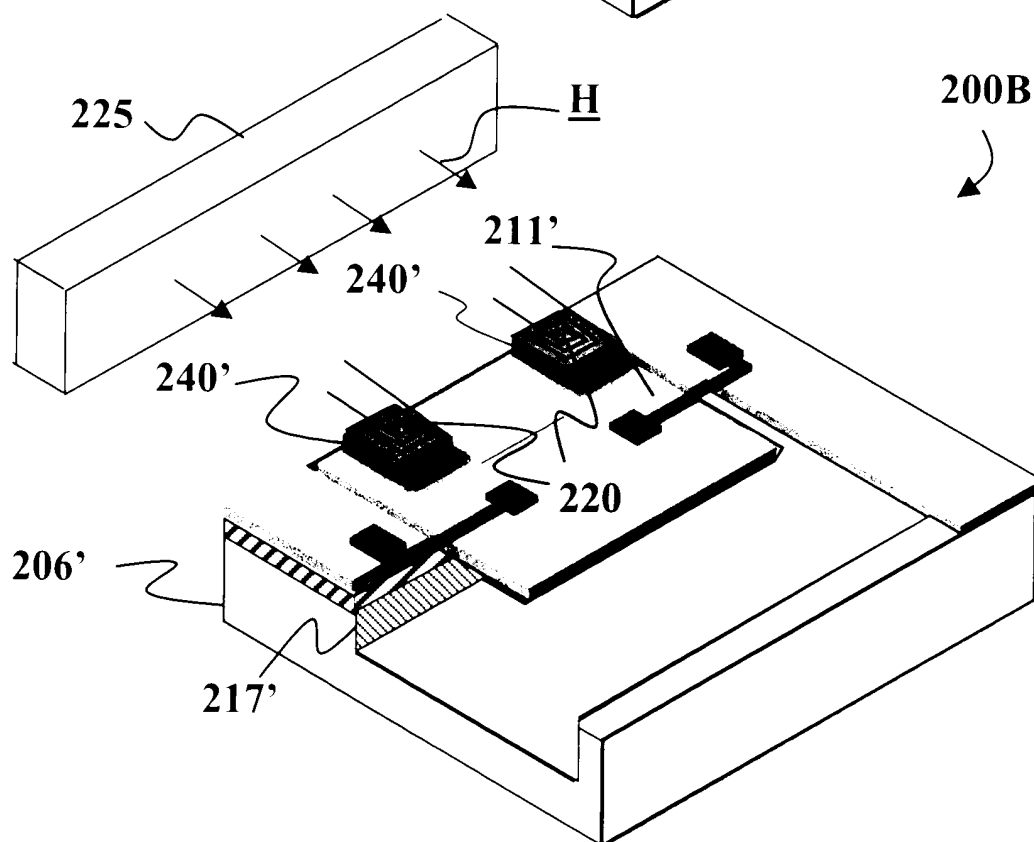


Fig. 2B

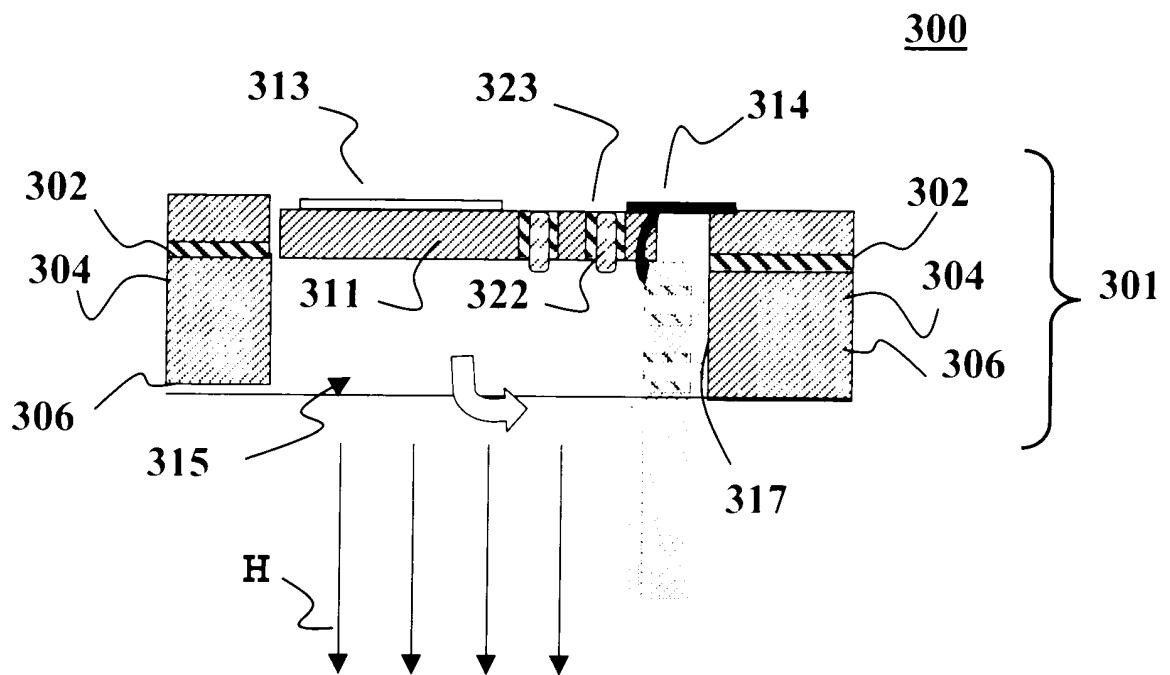


Fig. 3A

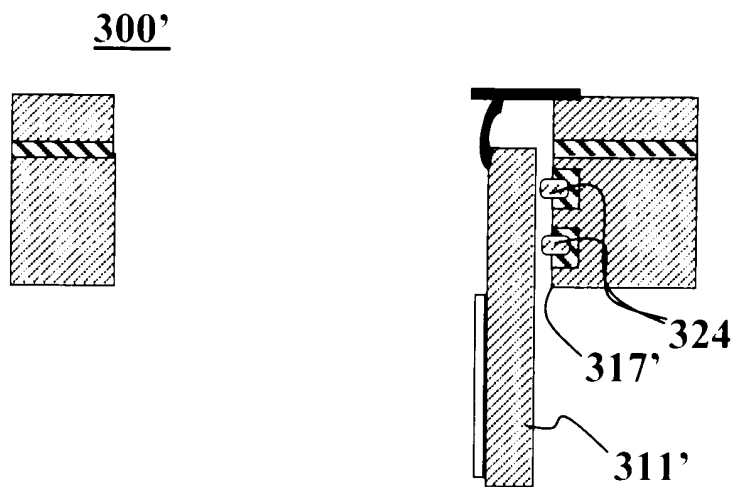


Fig. 3B

Fig. 4A



Fig. 4B



Fig. 4C



Fig. 4D



Fig. 4E

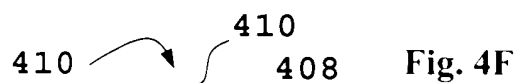
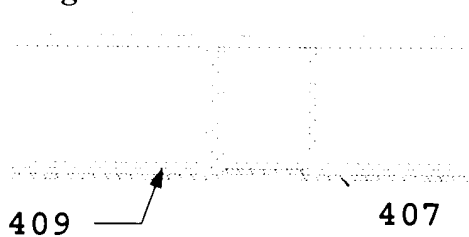


Fig. 4F

FIG. 4G is a schematic diagram of a device in accordance with the present invention.

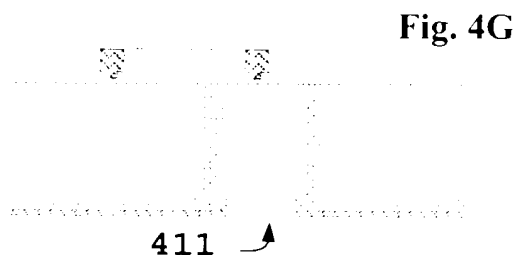


Fig. 4G

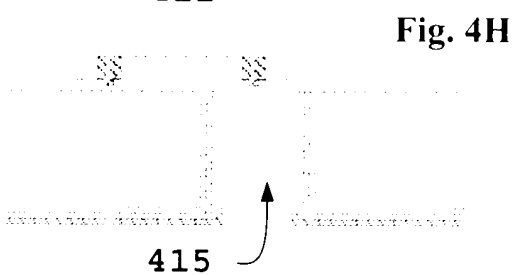


Fig. 4H

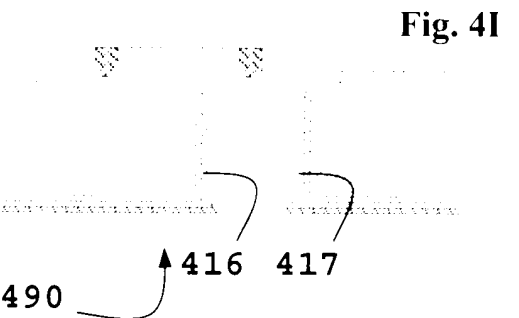


Fig. 4I

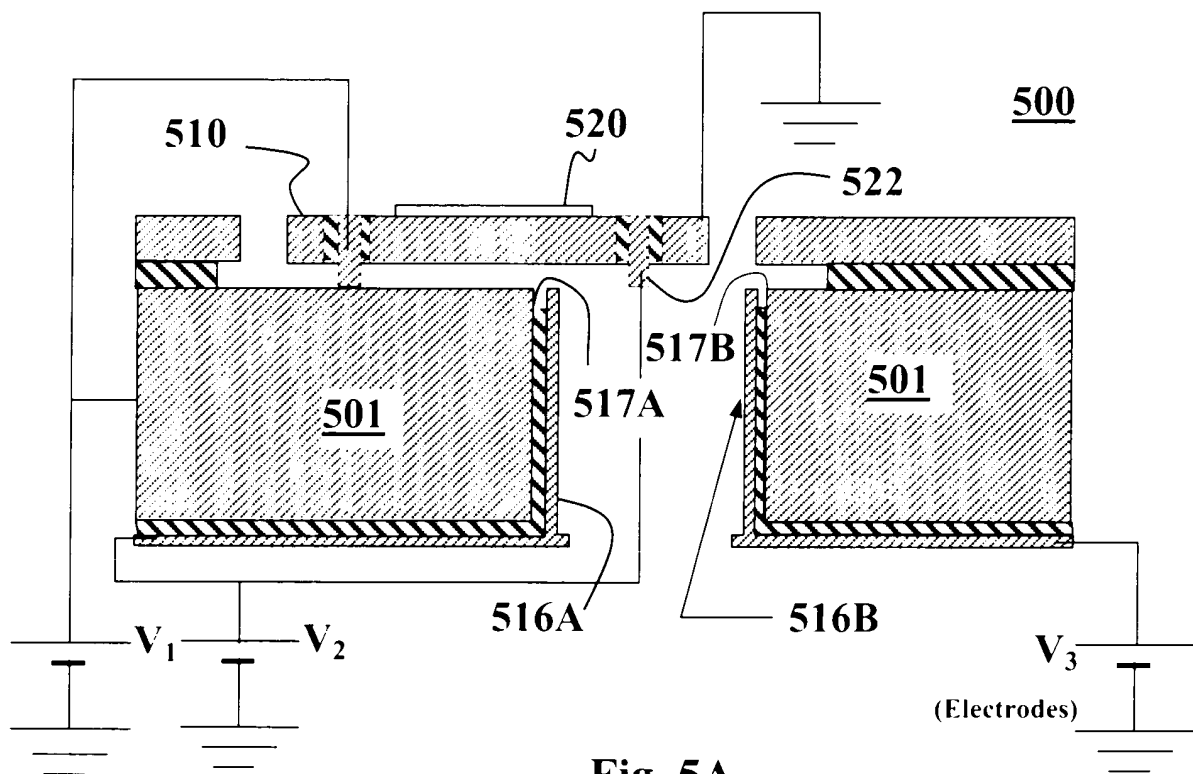


Fig. 5A

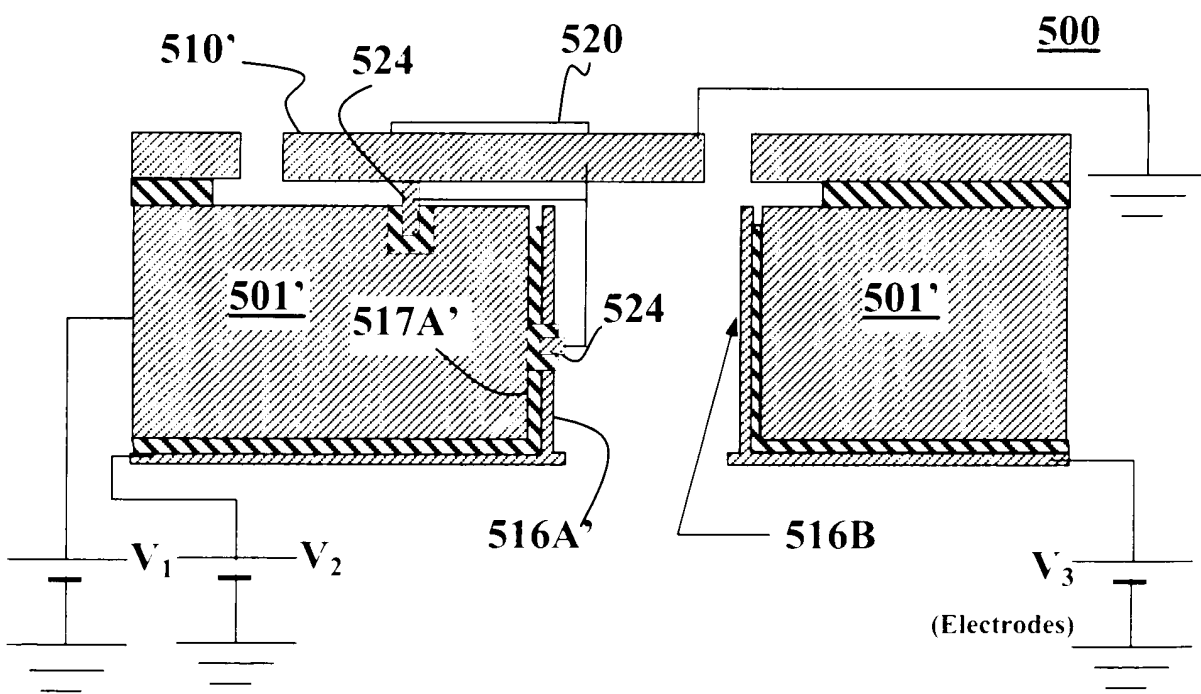


Fig. 5B

600

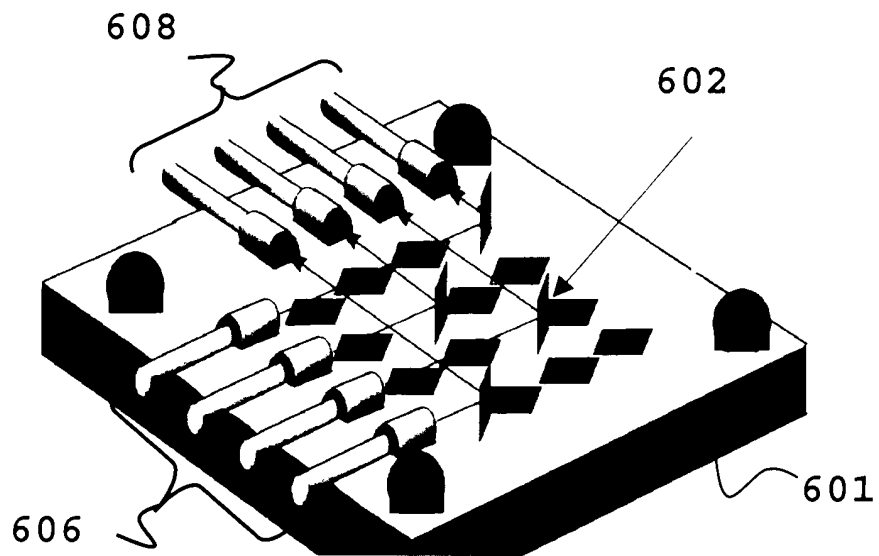


Fig. 6

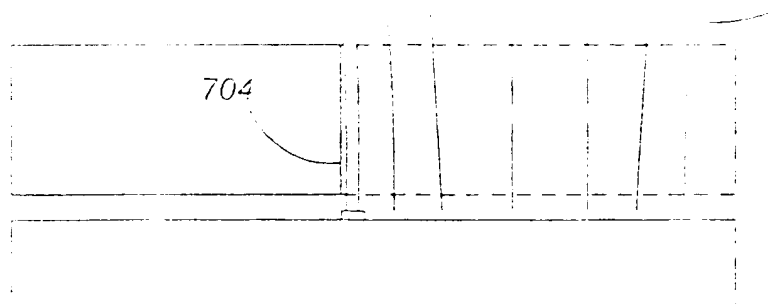


FIG. 7a

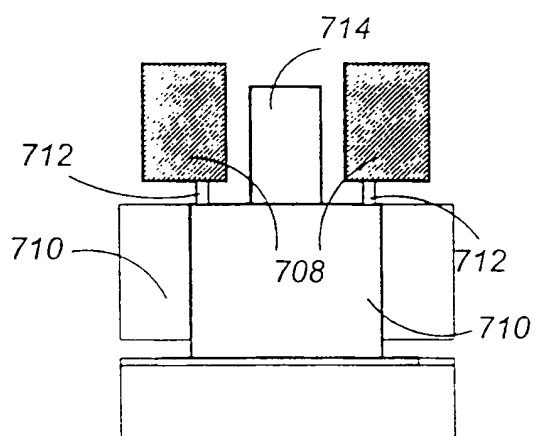


FIG. 7b

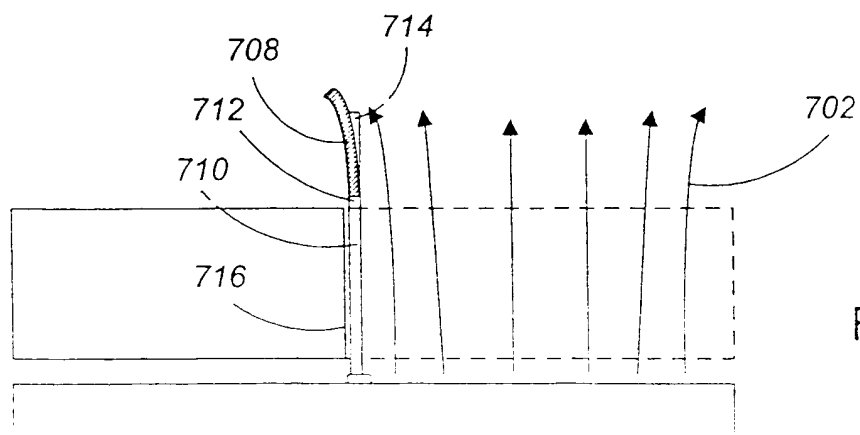


FIG. 7c